**B. DEEKSHITHA**

S-6, 3rd Stage,

Kirloskar Colony,

Basaveshwaranagar

Bengaluru-560079

deeksha11296@gmail.com

+91 7026989890

**Career Objective:**

To work in a challenging environment demanding all my skills and efforts to explore and adapt myself in different fields, and realize my potential and contribute to the development of organisation with impressive performance.

**Academic Qualification:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **COURSE** | **INSTITUTE** | **BOARD/UNIVERSITY** | **YEAR** | **AGGRREGATE** |
| Bachelor of Engineering(ECE) | Rajarajeswari college of Engineering | Visvesvaraya Technological University, Belagavi | 2018 | 72.82%(6th sem) |
| Pre- university  (PCMB) | K.L.E society’s S.Nijalingappa PU college | State board of Karnataka | 2014 | 83.2% |
| SSLC | St.Mira’s High School | State board of Karnataka | 2012 | 90.71% |

**Skill Profile:**

**Programming Languages:** Basics of C & C++, HDL, Arduino, microprocessor and microcontroller.

**Tools used:** MATLAB, Xilinx, MASM, keil.

**Academic Projects:**

1. **Title:** “Diabetic foot neuropathy monitoring system” .(mini project -2nd year BE)

**Description:** Using PZT sensors arduino UNO controller to detect foot ulcers.

**Papers Published:**

**Paper Title:** “Survey on different methods of image steganography”.

**Conference:**2nd National conference on VLSI communication and signal processing (NCVCS-2017) may 16-17, 2017 at Rajarajeswari college of Engineering, Bangalore

**Extra Curricular Activities:**

* Secured 1st place in Poster presentation on the topic “**Radiation hazards**” at National level technical symposium Electroblitz season-1
* Secured Best paper award on the topic“**Image Steganography**” at 2nd National Conference on VLSI.(NCVCS-2017)
* Secured 1st place in **impromptu** at national level technical symposium Electroblitz season-2
* Participated in workshops organised by the department namely Android Technology, Design Thinking, innovation and implementation using Intel Edison Boards, Open Hardware projects, Proteus Simulation and PCB Design, Touch of PLC in real world applications.
* Taken part in the workshop on wireless sensors networks conducted by Nihon Communication solutions and CoreEL technologies.
* Attended NCC CATC and got NCC ’A’ Certificate.
* Actively Participated and won prizes in many cultural and technical activities and games at school and college level.

**Internship:**

Internship program in Gemni Electro Corporation on a project titled ”Design, Assembly and testing of liner power supply”.

**Areas Of Strength:**

* Confident and a good listener, Self motivated with positive attitude
* Ability to deal with people diplomatically
* Team worker and flexible
* Enthusiastic to learn

**Personal Interest:**

Cooking, watching animations.

**Personal Profile:**

Name : B Deekshitha

Date of Birth : 01/12/1996

Father’s Name: B Gunasekhar Reddy

Mother’s Name: B Mallika

Nationality: Indian

Linguistic proficiency: English, Kannada, Telugu

**Declaration:**

I here by solemnly affirm that all information furnished above is true to the best of my knowledge and belief.

**Place**: Bengaluru (B Deekshitha)